



#### SINGLE BUFFER WITH OPEN DRAIN OUTPUT

## Description

The 74LVC1G07Q is an automotive-compliant, single buffer with an open-drain output The device is designed for operation with a power supply range of 1.65V to 5.5V. The inputs are tolerant to 5.5V allowing this device to be used in a mixed voltage environment. The device is fully specified for partial power down applications using  $I_{OFF}$ . The  $I_{OFF}$  circuitry disables the output preventing damaging current backflow when the device is powered down. The open-drain output can be connected to other open drain outputs to implement active-low wired-OR or active-high wired-AND functions. The maximum sink current is 32mA at 5V.

### **Pin Assignments**

**Applications** 

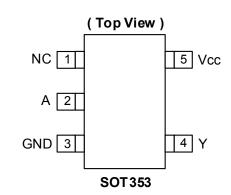
Voltage Level Shifting

General Purpose Logic

Range

Power Down Signal Isolation

Wide Array of Products such as:



Automotive applications within Grade 1 Temperature

Industrial Computing/Controls/Automation

Industrial/Agricultural Equipment

High Reliability Networking/Communications

### Features

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- Grade 1 Ambient Temperature Operation: -40°C to 125°C
- Wide Supply Voltage Range from 1.65V to 5.5V
- 24mA Sink Current at 3.3V
- CMOS Low Power Consumption
- IOFF Supports Partial-Power-Down Mode Operation
- Inputs Accept up to 5.5V Regardless of Vcc Level
- ESD Protection Tested per AEC-Q100
- Exceeds 2000V Human Body Model (AEC Q100-002)
- Exceeds 1000V Charged Device Model (AEC Q100-011)
- Latch-Up Exceeds 100mA (AEC Q100-004)
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The 74LVC1G07Q is suitable for automotive applications requiring specific change control and is AEC-Q100 qualified, has a grade 1 -40°C to 125°C temperature rating, is PPAP capable, and is manufactured in IATF16949:2016 certified facilities.

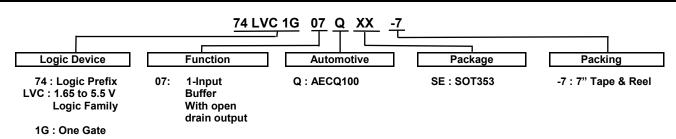
#### Notes:

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.



### Ordering Information (Note 4)



Part Number	Package	Package	Package	7" Tape ar	nd Reel
Fart Number	Code	(Notes 5 & 6)	Size	Quantity	Part Number Suffix
74LVC1G07QSE-7	SE	SOT353	2.0mm × 2.0mm × 1.1mm 0.65mm lead pitch	3000/Tape & Reel	-7

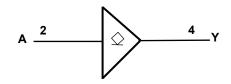
Notes:

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.
5. Pad layout as shown in Diodes Inc. suggested pad layouts, which can be found on our website at see http://www.diodes.com/package-outlines.html.
6. The taping orientation is located on our website at http://www.diodes.com/datasheets/ap02007.pdf.

## **Pin Descriptions**

Pin Name	Description
NC	No Connection
А	Data Input
GND	Ground
Y	Data Output
V <sub>CC</sub>	Supply Voltage

# Logic Diagram



## **Function Table**

Inputs	Output
Α	Y
Н	Z
L	L



## Absolute Maximum Ratings (Notes 7 & 8)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	kV
ESD CDM	Charged Device Model ESD Protection	1	kV
Vcc	Supply Voltage Range	-0.5 to 6.5	V
VI	Input Voltage Range	-0.5 to 6.5	V
Vo	Voltage Applied to Output in High Impedance or IOFF State	-0.5 to 6.5	V
Vo	Voltage Applied to Output in High or Low State	-0.5 to V <sub>CC</sub> +0.5	V
lıк	Input Clamp Current VI<0	-50	mA
Іок	Output Clamp Current	-50	mA
Ι <sub>Ο</sub>	Continuous Output Current	50	mA
I <sub>CC,</sub> I <sub>GND</sub>	Continuous Current Through V <sub>CC</sub> or GND	±100	mA
TJ	Operating Junction Temperature	-40 to +150	°C
T <sub>STG</sub>	Storage Temperature	-65 to +150	°C

Notes: 7. Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.

8. Forcing the maximum allowed voltage could cause a condition exceeding the maximum current or conversely forcing the maximum current could cause a condition exceeding the maximum voltage. The ratings of both current and voltage must be maintained within the controlled range.

Symbol		Parameter	Min	Max	Unit	
V	Operating Voltage	Operating Voltage		5.5	V	
V <sub>CC</sub>	Operating voltage	Data retention only	1.5	—	V	
		V <sub>CC</sub> = 1.65V to 1.95V	0.65 × V <sub>CC</sub>	—		
N/	Llich Lovel Input Veltage	V <sub>CC</sub> = 2.3V to 2.7V	1.7	—	v	
VIH	High-Level Input Voltage	$V_{CC}$ = 3V to 3.6V	2	—	v	
		V <sub>CC</sub> = 4.5V to 5.5V	0.7 × V <sub>CC</sub>	—		
		V <sub>CC</sub> = 1.65V to 1.95V	—	0.35 × V <sub>CC</sub>		
VIL	Low-Level Input Voltage	V <sub>CC</sub> = 2.3V to 2.7V	—	0.7		
		V <sub>CC</sub> = 3V to 3.6V	—	0.8	V	
		V <sub>CC</sub> = 4.5V to 5.5V	—	0.3 × V <sub>CC</sub>		
VI		Input Voltage	0	5.5	V	
Vo	C	Dutput Voltage	0	5.5	V	
		V <sub>CC</sub> = 1.65V	—	4		
		V <sub>CC</sub> = 2.3V	—	8		
1		V <sub>CC</sub> = 2.7V	—	12	-	
IOL	Low-Level Output Current	$\gamma = 2\gamma$	—	16	mA	
		V <sub>CC</sub> = 3V	—	24		
		$V_{CC} = 4.5V$	—	32		
		$V_{CC}$ = 1.8V ± 0.15V, 2.5V ± 0.2V	—	20		
	Input Transition Rise or Fall Rate	$V_{CC} = 3.3V \pm 0.3V$	—	10	ns/V	
		$V_{CC} = 5V \pm 0.5V$	—	5		
T <sub>A</sub>	Operating Free-Air Temperature	_	-40	+125	°C	

## Recommended Operating Conditions (Note 9)

Note: 9. Unused inputs should be held at  $V_{CC}$  or Ground.



### **Electrical Characteristics** (All typical values are at V<sub>CC</sub> = 3.3V, T<sub>A</sub> = +25°C)

Sumphal	Parameter	Teat C	Test Conditions		-40°C to +125°C			Unit
Symbol	Parameter	Test conditions		Vcc	Min	Тур	Мах	Unit
			I <sub>OL</sub> = 100μA	1.65V to 5.5V	_	—	0.1	
			I <sub>OL</sub> = 4mA	1.65V	_	—	0.45	
			I <sub>OL</sub> = 8mA	2.3V	_	—	0.3	
V <sub>OL</sub> Low I	Low Level Output Voltage	$V_{I} = V_{IL}$	I <sub>OL</sub> = 12mA	2.7V	_	—	0.4	V
			I <sub>OL</sub> = 24mA	3V	_	—	0.55	
			I <sub>OL</sub> = 32mA	4.5V	_	—	0.55	1
l <sub>l</sub>	Input Current	V <sub>I</sub> = 5.5V or G	ND	0 to 5.5V	_	± 0.1	±1	μA
I <sub>OFF</sub>	Power Down Leakage Current	$V_1 \text{ or } V_0 = 5.5$	/	0V	_	_	±2	μA
I <sub>OZ</sub>	Z-state Leakage Current	$V_I = V_{IH} V_O = 3$	5.5V	1.65V or 5.5V		± 0.1	±2	μA
I <sub>CC</sub>	Supply Current	$V_{\rm I}$ = 5.5V or GND I <sub>O</sub> =0		5.5V	_	0.1	4	μA
Δlcc	Additional Supply Current	Input at V <sub>CC</sub> –0.6V		3V to 5.5V	_	_	500	μA
CI	Input Capacitance	$V_i = V_{CC}$ to GN	ID	3.3V	_	5.0	_	pF

# Package Characteristics

Symbol	Parameter	Test Conditions	Vcc	Min	Тур.	Max	Unit
$\theta_{JA}$	Thermal Resistance Junction-to-Ambient	SOT353	Note 10	_	371	_	°C/W
θ <sub>JC</sub>	Thermal Resistance Junction-to-Case	SOT353	Note 10	_	143	_	°C/W

Note: 10. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

# **Switching Characteristics**

Figure 1 Typical Values at  $T_A$  = +25°C and nominal voltages 1.8V, 2.5V, 2.7V, 3.3V, and 5.0V.

Parameter	From	То	V.	T <sub>A</sub> = -40°C to 125°C			Unit
Parameter	Input	Output	Vcc	Min	Тур	Max	Unit
			1.8V ± 0.15V	1.0	2.6	8.4	
			2.5V ± 0.2V	0.5	1.7	7.0	
t <sub>PD</sub>	А	Y	2.7V	0.5	2.3	6.0	ns
		3.3V ± 0.3V	0.5	2.2	5.5		
			5.0V ± 0.5V	0.5	1.6	4.5	

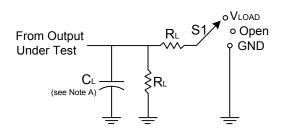
## **Operating Characteristics**

T<sub>A</sub> = +25°C

Barameter		Test	V <sub>CC</sub> = 1.8V	V <sub>CC</sub> = 2.5V	V <sub>CC</sub> = 3.3V	V <sub>CC</sub> = 5V	Unit
	Parameter	Conditions	Тур	Тур	Тур	Тур	Unit
C <sub>pd</sub>	Power Dissipation Capacitance	f = 10 MHz	5	5	6	6	pF



## **Measurement Information**

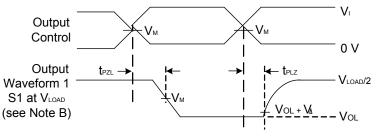


TEST	S1	CLRL
t <sub>PLZ</sub> /t <sub>PZL</sub>	Vload	Per Table

N N	Inputs			N N	0	6	VΔ
Vcc	VI	t <sub>r</sub> /t <sub>f</sub>	V <sub>M</sub>	VLOAD	C∟	R∟	VΔ
1.8V±0.15V	V <sub>CC</sub>	≤2ns	V <sub>CC</sub> /2	2 X V <sub>CC</sub>	30pF	1kΩ	0.15V
2.5V±0.2V	V <sub>CC</sub>	≤2ns	V <sub>CC</sub> /2	$2 \times V_{CC}$	30pF	500Ω	0.15V
2.7V	2.7V	≤2.5ns	1.5V	6V	50pF	500Ω	0.3V
3.3V±0.3V	3V	≤2.5ns	1.5V	6V	50pF	500Ω	0.3V
5V±0.5V	Vcc	≤2.5ns	V <sub>CC</sub> /2	2 X V <sub>CC</sub>	50pF	500Ω	0.3V



Voltage Waveform Pulse Duration



Voltage Waveform Enable and Disable Times Low and High Level Enabling

#### Figure 1 Load Circuit and Voltage Waveforms

Notes:

A. Includes test lead and test apparatus capacitance. B. All pulses are supplied at pulse repetition rate  $\leq$  10 MHz

- C. The input is one transition per measurement.
- D. For the open drain device  $t_{\mathsf{PLZ}}$  and  $t_{\mathsf{PZL}}$  are the same as  $t_{\mathsf{PD}}$

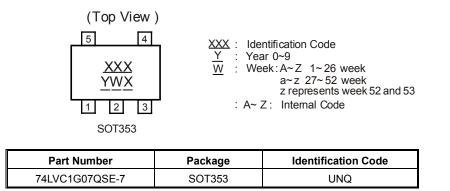
E. t<sub>PZL</sub> is measured at V<sub>M</sub>.

F. tPLz is measured at VOL +V $_{\Delta}$ 



## **Marking Information**

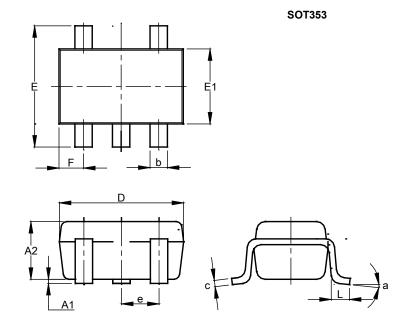
#### SOT353





## **Package Outline Dimensions**

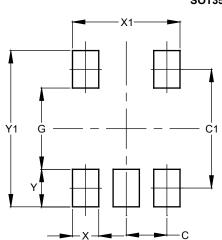
Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT353								
Dim	Min	Max	Тур					
A1	0.00	0.10	0.05					
A2	0.90	1.00	0.95					
b	0.10	0.30	0.25					
c	0.10	0.22	0.11					
D	1.80	2.20	2.15					
Е	2.00	2.20	2.10					
E1	1.15	1.35	1.30					
е	0	).650 B	SC					
F	0.40	0.45	0.425					
L	0.25	0.40	0.30					
а	0°	8°						
All	Dimen	sions	in mm					

## **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	0.650
C1	1.900
G	1.300
X	0.420
X1	1.720
Y	0.600
Y1	2.500

SOT353



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