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# DS75176B/DS75176BT Multipoint RS-485/RS-422 Transceivers

#### **General Description**

The DS75176B is a high speed differential TRI-STATE® bus/line transceiver designed to meet the requirements of EIA standard RS485 with extended common mode range (+12V to -7V), for multipoint data transmission. In addition, it is compatible with RS-422.

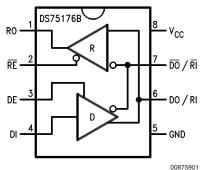
The driver and receiver outputs feature TRI-STATE capability, for the driver outputs over the entire common mode range of +12V to -7V. Bus contention or fault situations that cause excessive power dissipation within the device are handled by a thermal shutdown circuit, which forces the driver outputs into the high impedance state.

DC specifications are guaranteed over the 0 to 70°C temperature and 4.75V to 5.25V supply voltage range.

#### **Features**

- Meets EIA standard RS485 for multipoint bus transmission and is compatible with RS-422.
- Small Outline (SO) Package option available for minimum board space.
- 22 ns driver propagation delays.
- Single +5V supply.
- -7V to +12V bus common mode range permits ±7V ground difference between devices on the bus.
- Thermal shutdown protection.
- High impedance to bus with driver in TRI-STATE or with power off, over the entire common mode range allows the unused devices on the bus to be powered down.
- Pin out compatible with DS3695/A and SN75176A/B.
- Combined impedance of a driver output and receiver input is less than one RS485 unit load, allowing up to 32 transceivers on the bus.
- 70 mV typical receiver hysteresis.

#### **Connection and Logic Diagram**



Top View

Order Number DS75176BN, DS75176BTN, DS75176BM or DS75176BTM See NS Package Number N08E or M08A

## **Absolute Maximum Ratings** (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

Supply Voltage, V <sub>CC</sub>	7V
Control Input Voltages	7V
Driver Input Voltage	7V
Driver Output Voltages	+15V/ -10V
Receiver Input Voltages (DS75176B)	+15V/ -10V
Receiver Output Voltage	5.5V
Continuous Power Dissipation @	
25°C	
for M Package	675 mW (Note 5)
for N Package	900 mW (Note 4)

Lead Temperature

Storage Temperature Range

(Soldering, 4 seconds)

ESD Rating (HBM)

#### 500V

#### **Recommended Operating Conditions**

	Min	Max	Units
Supply Voltage, V <sub>CC</sub>	4.75	5.25	V
Voltage at Any Bus Terminal	-7	+12	V
(Separate or Common Mode)			
Operating Free Air Temperature $T_A$			
DS75176B	0	+70	°C
DS75176BT	-40	+85	°C
Differential Input Voltage,			
VID (Note 6)	-12	+12	V

#### **Electrical Characteristics** (Notes 2, 3)

 $0^{\circ}\text{C} \le \text{T}_{\text{A}} \le 70^{\circ}\text{C}$ , 4.75V <  $\text{V}_{\text{CC}} <$  5.25V unless otherwise specified

-65°C to +150°C

260°C

Symbol	Parame	ter	Conditions		Min	Тур	Max	Units
V <sub>OD1</sub>	Differential Driver Output I <sub>O</sub> = 0					5	V	
	Voltage (Unloaded)							
V <sub>OD2</sub>	Differential Driver Outp	ut	(Figure 1)	R = 50Ω; (RS-422) (Note 7)	2			V
	Voltage (with Load)			$R = 27\Omega$ ; (RS-485)	1.5			V
$\Delta V_{OD}$	Change in Magnitude of Driver							
	Differential Output Voltage For						0.2	V
	Complementary Output	t States						
V <sub>oc</sub>	Driver Common Mode	Output	(Figure 1)	$R = 27\Omega$			3.0	V
	Voltage							
$\Delta  V_{OC} $	Change in Magnitude of	of Driver						
	Common Mode Output	Voltage					0.2	V
	For Complementary Output							
	States							
V <sub>IH</sub>	Input High Voltage				2			V
V <sub>IL</sub>	Input Low Voltage		DI, DE,				0.8	
V <sub>CL</sub>	Input Clamp Voltage		RE, E	$I_{IN} = -18 \text{ mA}$			-1.5	
I <sub>IL</sub>	Input Low Current			$V_{IL} = 0.4V$			-200	μΑ
I <sub>IH</sub>	Input High Current		$V_{IH} = 2.4V$				20	μΑ
I <sub>IN</sub>	Input	DO/RI, DO/RI	$V_{CC} = 0V \text{ or } 5.25V$	V <sub>IN</sub> = 12V			+1.0	mA
	Current		DE = 0V	$V_{IN} = -7V$			-0.8	mA
$V_{TH}$	Differential Input Thres	hold	$-7V \le V_{CM} \le + 12V$		-0.2		+0.2	V
	Voltage for Receiver							
$\Delta V_{TH}$	Receiver Input Hystere	sis	V <sub>CM</sub> = 0V			70		mV
$V_{OH}$	Receiver Output High	Voltage	oltage $I_{OH} = -400  \mu A$		2.7			V
$V_{OL}$	Output Low Voltage	RO	I <sub>OL</sub> = 16 mA (Note 7)				0.5	V
I <sub>OZR</sub>	OFF-State (High Imped	dance)	V <sub>CC</sub> = Max				±20	μΑ
	Output Current at Rece	eiver	$0.4V \le V_O \le 2.4V$					
R <sub>IN</sub>	Receiver Input Resista	nce	nce $-7V \le V_{CM} \le +12V$		12			kΩ
I <sub>CC</sub>	Supply Current		No Load	Driver Outputs Enabled			55	mA
			(Note 7)	Driver Outputs Disabled			35	mA

## Electrical Characteristics (Notes 2, 3) (Continued)

 $0^{\circ}\text{C} \le \text{T}_{\text{A}} \le 70^{\circ}\text{C}$ , 4.75V <  $\text{V}_{\text{CC}} <$  5.25V unless otherwise specified

Symbol	Parameter	Conditions	Min	Тур	Max	Units
I <sub>OSD</sub>	Driver Short-Circuit	$V_O = -7V$ (Note 7)			-250	mA
	Output Current	V <sub>O</sub> = +12V (Note 7)			+250	mA
I <sub>OSR</sub>	Receiver Short-Circuit	$V_O = 0V$	-15		-85	mA
	Output Current					

**Note 1:** "Absolute Maximum Ratings" are those beyond which the safety of the device cannot be guaranteed. They are not meant to imply that the device should be operated at these limits. The tables of "Electrical Characteristics" provide conditions for actual device operation.

Note 2: All currents into device pins are positive; all currents out of device pins are negative. All voltages are referenced to device ground unless otherwise specified.

Note 3: All typicals are given for  $V_{CC} = 5V$  and  $T_A = 25^{\circ}C$ .

Note 4: Derate linearly at 5.56 mW/°C to 650 mW at 70°C.

Note 5: Derate linearly @ 6.11 mW/°C to 400 mW at 70°C.

Note 6: Differential - Input/Output bus voltage is measured at the noninverting terminal A with respect to the inverting terminal B.

Note 7: All worst case parameters for which note 7 is applied, must be increased by 10% for DS75176BT. The other parameters remain valid for -40°C < T<sub>A</sub> < +85°C.

#### **Switching Characteristics**

 $V_{CC} = 5.0V, T_A = 25^{\circ}C$ 

Symbol	Parameter	Conditions	Min	Тур	Max	Units
t <sub>PLH</sub>	Driver Input to Output	$R_{LDIFF} = 60\Omega$		12	22	ns
t <sub>PHL</sub>	Driver Input to Output	$C_{L1} = C_{L2} = 100 \text{ pF}$		17	22	ns
t <sub>r</sub>	Driver Rise Time	$R_{LDIFF} = 60\Omega$			18	ns
t <sub>f</sub>	Driver Fall Time	$C_{L1} = C_{L2} = 100 \text{ pF}$			18	ns
		(Figure 3 and Figure 5)				
t <sub>zH</sub>	Driver Enable to Output High	C <sub>L</sub> = 100 pF ( <i>Figure 4</i> and <i>Figure 6</i> ) S1		29	100	ns
		Open				
$t_{ZL}$	Driver Enable to Output Low	$C_L = 100 \text{ pF } (Figure 4 \text{ and } Figure 6) \text{ S2}$		31	60	ns
		Open				
$t_{LZ}$	Driver Disable Time from Low	$C_L = 15 \text{ pF} (Figure 4 \text{ and } Figure 6) S2$		13	30	ns
		Open				
$t_{HZ}$	Driver Disable Time from High	$C_L = 15 \text{ pF} (Figure 4 \text{ and } Figure 6) \text{ S1}$		19	200	ns
		Open				
t <sub>PLH</sub>	Receiver Input to Output	$C_L = 15 \text{ pF } (Figure 2 \text{ and } Figure 7)$		30	37	ns
$t_{PHL}$	Receiver Input to Output	S1 and S2 Closed		32	37	ns
$t_{ZL}$	Receiver Enable to Output Low	C <sub>L</sub> = 15 pF ( <i>Figure 2</i> and <i>Figure 8</i> ) S2		15	20	ns
		Open				
$t_{ZH}$	Receiver Enable to Output High	$C_L = 15 \text{ pF} (Figure 2 \text{ and } Figure 8) S1$		11	20	ns
		Open				
$t_{LZ}$	Receiver Disable from Low	C <sub>L</sub> = 15 pF ( <i>Figure 2</i> and <i>Figure 8</i> ) S2		28	32	ns
		Open				
$t_{HZ}$	Receiver Disable from High	C <sub>L</sub> = 15 pF ( <i>Figure 2</i> and <i>Figure 8</i> ) S1		13	35	ns
		Open				

#### **AC Test Circuits**

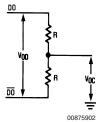
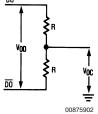
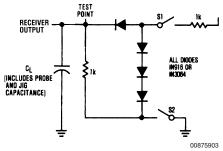


FIGURE 1.





Note: S1 and S2 of load circuit are closed except as otherwise mentioned.

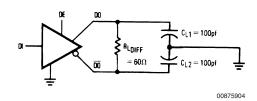
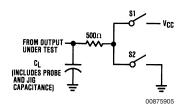


FIGURE 3.



Note: Unless otherwise specified the switches are closed.

FIGURE 4.

FIGURE 2.

# **Switching Time Waveforms**

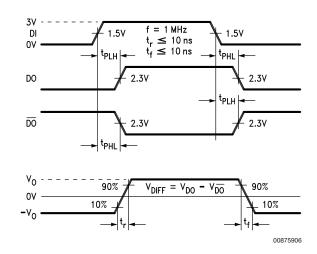


FIGURE 5. Driver Propagation Delays and Transition Times

# Switching Time Waveforms (Continued)

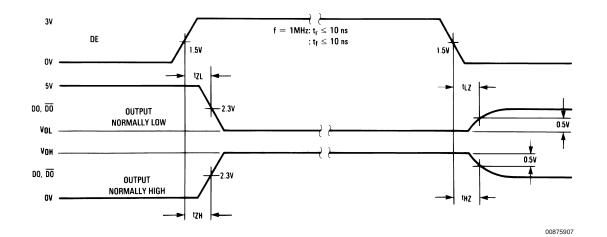
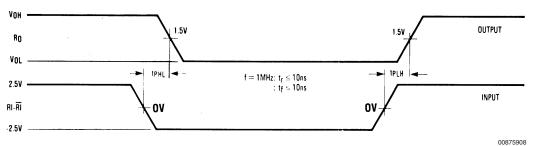


FIGURE 6. Driver Enable and Disable Times



Note: Differential input voltage may may be realized by grounding  $\overline{\text{RI}}$  and pulsing RI between +2.5V and -2.5V

FIGURE 7. Receiver Propagation Delays

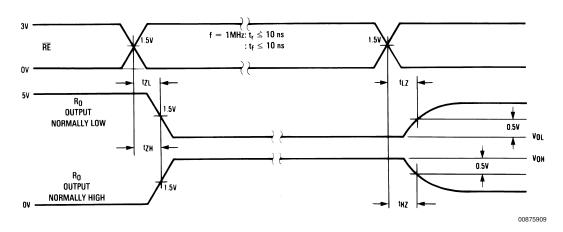


FIGURE 8. Receiver Enable and Disable Times

# **Function Tables DS75176B Transmitting**

Inputs		Line	Out	puts	
RE	DE	DI	Condition	DO	DO
Х	1	1	No Fault	0	1
X	1	0	No Fault	1	0
X	0	Х	Х	Z	Z
X	1	Х	Fault	Z	Z

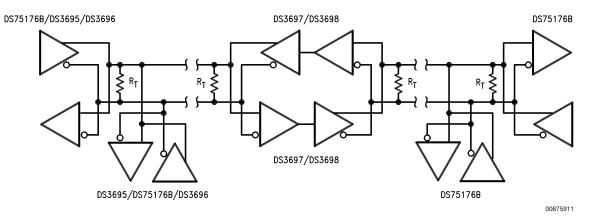
#### DS75176B Receiving

	Outputs		
RE	DE	RI-RI	RO
0	0	≥ +0.2V	1
0	0	≤ -0.2V	0
0	0	Inputs Open**	1
1	0	X	z

X — Don't care condition

Fault — Improper line conditions causing excessive power dissipation in the driver, such as shorts or bus contention situations
\*\*This is a fail safe condition

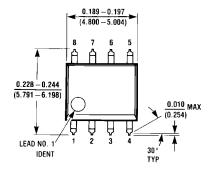
# **Typical Application**

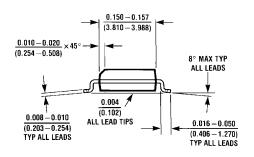


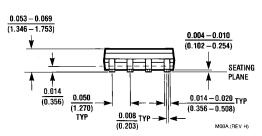
Z — High impedance state

#### Physical Dimensions inches (millimeters)

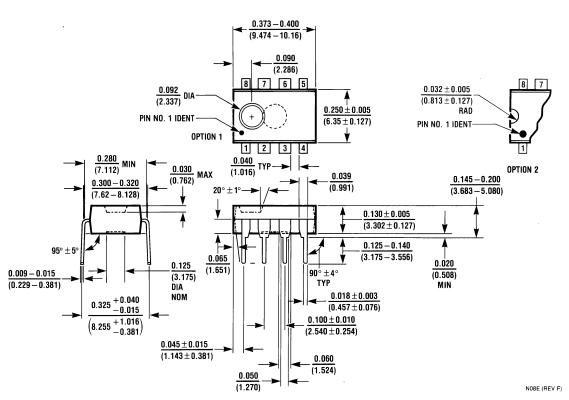
unless otherwise noted







Lit. # 103669



Molded Dual-In-Line Package (N)
Order Number DS75176BN or DS75176BTN
NS Package Number N08E

#### **Notes**

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