- Operates From 1.65 V to 3.6 V
- Inputs Accept Voltages to 5.5 V
- Max t_{pd} of 3.8 ns
- Typical V_{OLP} (Output Ground Bounce) <0.8 V at V_{CC} = 3.3 V, T_A = 25°C
- Typical V_{OHV} (Output V_{OH} Undershoot) >2 V at V_{CC} = 3.3 V, T_A = 25°C
- Inputs Accept Voltages to 5.5 V
- Latch-Up Performance Exceeds 100 mA Per JESD 78, Class II
- ESD Protection Exceeds JESD 22
  - 2000-V Human-Body Model (A114-A)
  - 200-V Machine Model (A115-A)
  - 1000-V Charged-Device Model (C101)

D, DB, DGV, NS, OR PW PACKAGE
(TOP VIEW)

1A	1	14	V_{CC}
1Y	2	13	6A
2A	3	12	6Y
2Y	4	11	5A
3A	5	10	5Y
3Y	6	9	4A
GND	7	8	4Y
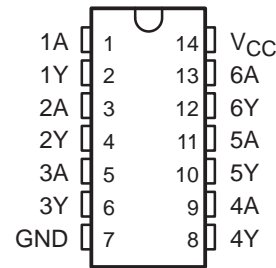
description/ordering information

This hex inverter is designed for 1.65-V to 3.6-V V_{CC} operation.

The SN74LVCU04A contains six independent inverters with unbuffered outputs and performs the Boolean function $Y = \overline{A}$.

Inputs can be driven from either 3.3-V or 5-V devices. This feature allows the use of these devices as translators in a mixed 3.3-V/5-V system environment.

ORDERING INFORMATION

T_A	PACKAGE†		ORDERABLE PART NUMBER	TOP-SIDE MARKING
−40°C to 85°C	SOIC – D	Tube of 50	SN74LVCU04AD	LVCU04A
		Reel of 2500	SN74LVCU04ADR	
		Reel of 250	SN74LVCU04ADT	
	SOP – NS	Reel of 2000	SN74LVCU04ANSR	LVCU04A
	SSOP – DB	Reel of 2000	SN74LVCU04ADBR	LCU04A
	TSSOP – PW	Tube of 90	SN74LVCU04APW	LCU04A
		Reel of 2000	SN74LVCU04APWR	
		Reel of 250	SN74LVCU04APWT	
	TVSOP – DGV	Reel of 2000	SN74LVCU04ADGVR	LCU04A

† Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at www.ti.com/sc/package.

FUNCTION TABLE
(each inverter)

INPUT A	OUTPUT Y
H	L
L	H

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logic diagram, each inverter (positive logic)

A ─────▷o───── Y
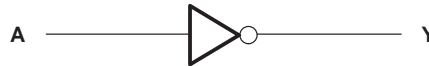
absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Supply voltage range, V_{CC} . . . . −0.5 V to 6.5 V
Input voltage range, V_I (see Note 1) . . . . −0.5 V to 6.5 V
Output voltage range, V_O (see Notes 1 and 2) . . . . −0.5 V to V_{CC} + 0.5 V
Input clamp current, I_{IK} ($V_I < 0$) . . . . −50 mA
Output clamp current, I_{OK} ($V_O < 0$) . . . . −50 mA
Continuous output current, I_O . . . . ±50 mA
Continuous current through V_{CC} or GND . . . . ±100 mA
Package thermal impedance, θ_{JA} (see Note 3): D package . . . . 86°C/W
                                                              DB package . . . . 96°C/W
                                                              DGV package . . . . 127°C/W
                                                              NS package . . . . 76°C/W
                                                              PW package . . . . 113°C/W
Storage temperature range, T_{stg} . . . . −65°C to 150°C

† Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and
functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not
implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
NOTES: 1. The input negative-voltage and output voltage ratings may be exceeded if the input and output current ratings are observed.
2. The value of V_{CC} is provided in the recommended operating conditions table.
3. The package thermal impedance is calculated in accordance with JESD 51-7.

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recommended operating conditions (see Note 4)

				MIN	MAX	UNIT
V_{CC}	Supply voltage		Operating	1.65	3.6	V
			Data retention only	1.5		
V_{IH}	High-level input voltage		V_{CC} = 1.65 V	1.32		V
			V_{CC} = 2.3 V	1.84		
			V_{CC} = 2.7 V	2.16		
			V_{CC} = 3 V	2.4		
			V_{CC} = 3.6 V	2.88		
V_{IL}	Low-level input voltage		V_{CC} = 1.65 V		0.4	V
			V_{CC} = 2.3 V		0.5	
			V_{CC} = 2.7 V to 3.6 V		0.65	
V_I	Input voltage			0	5.5	V
V_O	Output voltage			0	V_{CC}	V
I_{OH}	High-level output current		V_{CC} = 1.65 V		−4	mA
			V_{CC} = 2.3 V		−8	
			V_{CC} = 2.7 V		−12	
			V_{CC} = 3 V		−24	
I_{OL}	Low-level output current		V_{CC} = 1.65 V		4	mA
			V_{CC} = 2.3 V		8	
			V_{CC} = 2.7 V		12	
			V_{CC} = 3 V		24	
T_A	Operating free-air temperature			−40	85	°C

NOTE 4: All unused inputs of the device must be held at V_{CC} or GND to ensure proper device operation. Refer to the TI application report, *Implications of Slow or Floating CMOS Inputs*, literature number SCBA004.

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS		V_{CC}	MIN	TYP†	MAX	UNIT
V_{OH}	I_{OH} = −100 µA,	V_{IL} = 0 V	1.65 V to 3.6 V	V_{CC}−0.2			V
	I_{OH} = −4 mA,	V_{IL} = 0 V	1.65 V	1.2			
	I_{OH} = −8 mA,	V_{IL} =0 V	2.3 V	1.7			
	I_{OH} = −12 mA,	V_{IL} = 0 V	2.7 V	2.2			
			3 V	2.4			
	I_{OH} = −24 mA,	V_{IL} = 0 V	3 V	2.2			
V_{OL}	I_{OL} = 100 µA,	V_{IH} = V_{CC}	1.65 V to 3.6 V			0.2	V
	I_{OL} = 4 mA,	V_{IH} = V_{CC}	1.65 V			0.45	
	I_{OL} = 8 mA,	V_{IH} = V_{CC}	2.3 V			0.7	
	I_{OL} = 12 mA,	V_{IH} = V_{CC}	2.7 V			0.4	
	I_{OL} = 24 mA,	V_{IH} = V_{CC}	3 V			0.55	
I_I	V_I = 5.5 V or GND		3.6 V			±5	µA
I_{CC}	V_I = V_{CC} or GND,	I_O = 0	3.6 V			10	µA
ΔI_{CC}	One input at V_{CC} − 0.6 V,	Other inputs at V_{CC} or GND	2.7 V to 3.6 V			500	µA
C_i	V_I = V_{CC} or GND		3.3 V		5		pF

† All typical values are at V_{CC} = 3.3 V, T_A = 25°C.

switching characteristics over recommended operating free-air temperature range (unless otherwise noted) (see Figure 1)

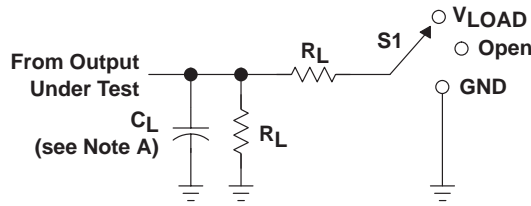| PARAMETER	FROM (INPUT)	TO (OUTPUT)	V_{CC} = 1.8 V $\pm$ 0.15 V		V_{CC} = 2.5 V $\pm$ 0.2 V		V_{CC} = 2.7 V		V_{CC} = 3.3 V $\pm$ 0.3 V		UNIT
			MIN	MAX	MIN	MAX	MIN	MAX	MIN	MAX	
t_{pd}	A	Y	1	7.3	1	6.7	1	4.7	1	3.8	ns
$t_{sk(o)}$										1	ns

operating characteristics, T_A = 25°C

PARAMETER		TEST CONDITIONS	V_{CC} = 1.8 V TYP	V_{CC} = 2.5 V TYP	V_{CC} = 3.3 V TYP	UNIT
C_{pd}	Power dissipation capacitance per inverter	f = 10 MHz	3	4	5	pF

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PARAMETER MEASUREMENT INFORMATION

LOAD CIRCUIT

TEST	S1
t_{PLH}/t_{PHL}	Open
t_{PLZ}/t_{PZL}	V_{LOAD}
t_{PHZ}/t_{PZH}	GND

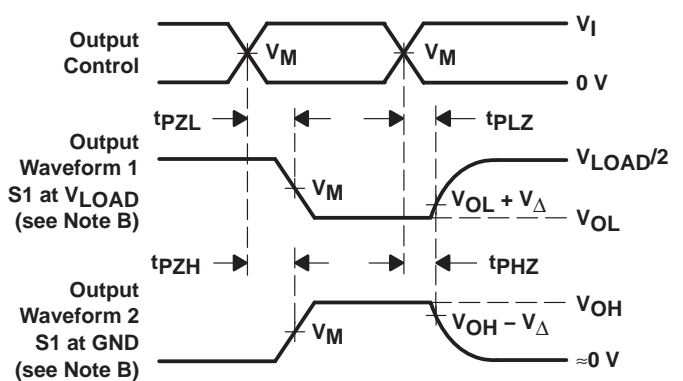| V_{CC}	INPUTS		V_M	V_{LOAD}	C_L	R_L	V_Δ
	V_I	t_r/t_f					
1.8 V ± 0.15 V	V_{CC}	≤2 ns	$V_{CC}/2$	$2 \times V_{CC}$	30 pF	1 kΩ	0.15 V
2.5 V ± 0.2 V	V_{CC}	≤2 ns	$V_{CC}/2$	$2 \times V_{CC}$	30 pF	500 Ω	0.15 V
2.7 V	2.7 V	≤2.5 ns	1.5 V	6 V	50 pF	500 Ω	0.3 V
3.3 V ± 0.3 V	2.7 V	≤2.5 ns	1.5 V	6 V	50 pF	500 Ω	0.3 V
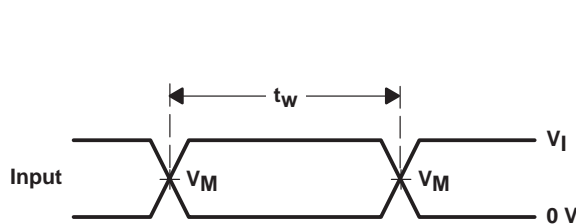
**VOLTAGE WAVEFORMS
PULSE DURATION**
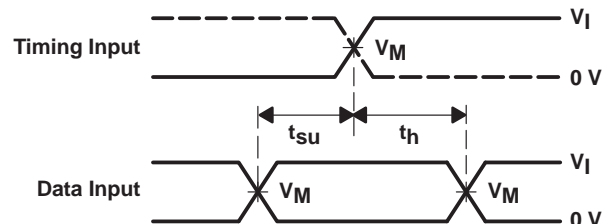
**VOLTAGE WAVEFORMS
SETUP AND HOLD TIMES**
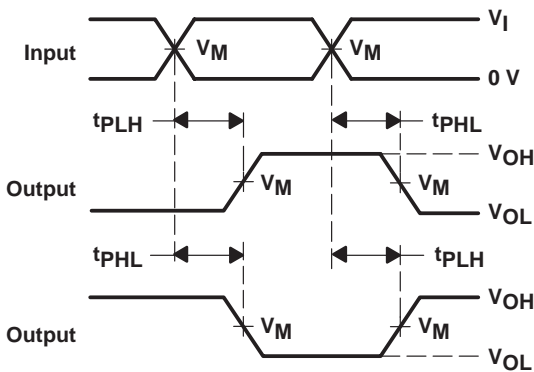
**VOLTAGE WAVEFORMS
PROPAGATION DELAY TIMES
INVERTING AND NONINVERTING OUTPUTS**

**VOLTAGE WAVEFORMS
ENABLE AND DISABLE TIMES
LOW- AND HIGH-LEVEL ENABLING**

NOTES: A. C_L includes probe and jig capacitance.
B. Waveform 1 is for an output with internal conditions such that the output is low, except when disabled by the output control.
Waveform 2 is for an output with internal conditions such that the output is high, except when disabled by the output control.
C. All input pulses are supplied by generators having the following characteristics: PRR ≤ 10 MHz, $Z_O = 50$ Ω.
D. The outputs are measured one at a time, with one transition per measurement.
E. t_{PLZ} and t_{PHZ} are the same as t_{dis}.
F. t_{PZL} and t_{PZH} are the same as t_{en}.
G. t_{PLH} and t_{PHL} are the same as t_{pd}.
H. All parameters and waveforms are not applicable to all devices.

Figure 1. Load Circuit and Voltage Waveforms

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DGV (R-PDSO-G**)   **PLASTIC SMALL-OUTLINE**

24 PINS SHOWN

PINS ** DIM	14	16	20	24	38	48	56
A MAX	3,70	3,70	5,10	5,10	7,90	9,80	11,40
A MIN	3,50	3,50	4,90	4,90	7,70	9,60	11,20

4073251/E 08/00

NOTES: A. All linear dimensions are in millimeters.
B. This drawing is subject to change without notice.
C. Body dimensions do not include mold flash or protrusion, not to exceed 0,15 per side.
D. Falls within JEDEC: 24/48 Pins – MO-153
14/16/20/56 Pins – MO-194

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D (R-PDSO-G**)   **PLASTIC SMALL-OUTLINE PACKAGE**

8 PINS SHOWN

0.050 (1,27)

0.020 (0,51) / 0.014 (0,35)

⊕ | 0.010 (0,25) Ⓜ

8

5

0.244 (6,20) / 0.228 (5,80)

0.157 (4,00) / 0.150 (3,81)

1

4

A

0.008 (0,20) NOM

Gage Plane

0°– 8°

0.010 (0,25)

0.044 (1,12) / 0.016 (0,40)

0.069 (1,75) MAX

0.010 (0,25) / 0.004 (0,10)

Seating Plane

0.004 (0,10)

	PINS **		
DIM	8	14	16
A MAX	0.197 (5,00)	0.344 (8,75)	0.394 (10,00)
A MIN	0.189 (4,80)	0.337 (8,55)	0.386 (9,80)

4040047/E 09/01

NOTES: A. All linear dimensions are in inches (millimeters).
         B. This drawing is subject to change without notice.
         C. Body dimensions do not include mold flash or protrusion, not to exceed 0.006 (0,15).
         D. Falls within JEDEC MS-012

NS (R-PDSO-G)**   PLASTIC SMALL-OUTLINE PACKAGE

14-PINS SHOWN

0,51 / 0,35   ⊕ 0,25 Ⓜ

1,27

14   8

5,60 / 5,00   8,20 / 7,40

1   7

A

0,15 NOM

Gage Plane

0°–10°   0,25

1,05 / 0,55

0,15 / 0,05

2,00 MAX

Seating Plane

⌂ 0,10

DIM \ PINS **	14	16	20	24
A MAX	10,50	10,50	12,90	15,30
A MIN	9,90	9,90	12,30	14,70

4040062/C 03/03

NOTES: A.   All linear dimensions are in millimeters.
          B.   This drawing is subject to change without notice.
          C.   Body dimensions do not include mold flash or protrusion, not to exceed 0,15.

DB (R-PDSO-G**)   **PLASTIC SMALL-OUTLINE**

28 PINS SHOWN

PINS ** DIM	14	16	20	24	28	30	38
A MAX	6,50	6,50	7,50	8,50	10,50	10,50	12,90
A MIN	5,90	5,90	6,90	7,90	9,90	9,90	12,30

4040065 /E 12/01

NOTES: A. All linear dimensions are in millimeters.
B. This drawing is subject to change without notice.
C. Body dimensions do not include mold flash or protrusion not to exceed 0,15.
D. Falls within JEDEC MO-150

PW (R-PDSO-G**)   **PLASTIC SMALL-OUTLINE PACKAGE**

14 PINS SHOWN

PINS ** DIM	8	14	16	20	24	28
A MAX	3,10	5,10	5,10	6,60	7,90	9,80
A MIN	2,90	4,90	4,90	6,40	7,70	9,60

4040064/F 01/97

NOTES: A. All linear dimensions are in millimeters.
B. This drawing is subject to change without notice.
C. Body dimensions do not include mold flash or protrusion not to exceed 0,15.
D. Falls within JEDEC MO-153

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Mailing Address: Texas Instruments

Post Office Box 655303 Dallas, Texas 75265

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